IEEE PELS/IES/IAS Jt. Chapter Meeting:


10:00h - 10:45h: Prof. KRISHNA SHENAI, Distinguished lecturer of the IEEE PELS University of Chicago, Chicago, Illinois, USA
“Reliability of Compact Power Systems”

10:45h – 11:00h: Refreshment

11:00h – 11:45h: Prof. PETAR GRBOVIC, Distinguished lecturer of the IEEE PELS University of Innsbruck, Innsbruck, Austria
“Multi-Cell & Multi-Level Power Converters - A Way to Go Beyond the Limits of Si”

Ee 2019 TUTORIALS:


12:00h – 15:00h: Tutorials Registration // Hard&Soft - Student Competition (Preliminaries)

13:00-13:30h OPENING - Tutorials // Hard&Soft Student Competition

13:30-18:00h Tutorial 1 - Mr. Darko Vračar, Brusa Elektronik (München) GmbH, Munich, Germany
"Development of power electronics’ systems - From blank sheet to mass production and beyond"

(Refreshment in hall)

13:30-18:00h Tutorial 2 - Prof. Drazen Dujic and Stefan Milovanovic
Power Electronics Laboratory – PEL, Swiss Federal Institute of Technology – EPFL, Lausanne, Switzerland
"Modular Multilevel Converters – Operating Principles and Applications"

(Refreshment in hall)

18:30-20:30h BEER PARTY (NS Craft Beers)

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**Ee 2019 CONFERENCE:**


09:00-18:30h Registration desk // Hard&Soft Student Competition

09:30-10:00h OPENING

10:00-10:40h Key Note - Prof. Dr. Seddik Bacha  
Grenoble Alpes University / G2Elab and Supergrid Institute, Grenoble/Lion, France  
"The renewables integration via HVDC grids"

10:40-11:20h Key Note - Dr. Michail Vasiladiotis  
ABB Switzerland, Turgi, Switzerland  
"Highly efficient and robust direct modular multilevel converters for grid-connected applications"

11:20-11:30h Refreshment

11:30-12:00h Invited paper - Dr. Ki-Bum Park  
ABB Corporate Research, Baden-Dättwil, Switzerland  
"Spread spectrum modulation for LCL grid-tied converter"

12:00-12:30h Invited paper - Dr. Sasa Djokic  
School of Engineering, University of Edinburgh, Edinburgh, Scotland, U.K.  
"Non-Fourier fundamental components for the mitigation of waveform distortions"

12:30-13:30h LUNCH

13:30-15:00h Industry session - 1

15:00-16:30h T1.1 // T4.1 (2x6 papers)

16:30-16:45h Refreshment

16:45-18:30h T1.2 // T7.1 (2x7 papers)

18:30-20:30h WELCOME RECEPTION


09:00-18:30h Registration desk // Hard&Soft Student Competition

09:30-10:30h T1.3 // T2.1 (2x4 papers)

10:30-11:10h Key Note – Prof. Dr. Marc Hiller  
University of Karlsruhe, Karlsruhe, Germany  
"Requirements for highly dynamic PHIL converter systems"

11:10-11:20h Refreshment

11:20-12:00h Key Note – Asst.Prof. Dr. Miroslav Vasic,  
Universidad Politecnica de Madrid, Madrid, Spain  
"Demystifying power GaN devices: theory and application"

12:00-12:30h Invited presentation - Dr. Eckhard Thal  
Mitsubishi Electric Europe, Ratingen, Germany  
"Evolution of high-power SiC modules"

12:30-13:30h LUNCH

12:30-16:00h Hard&Soft Student Competition - FINALE

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12:30-16:00h Hard&Soft Student Competition - FINALE

13:30 – 14:30 Industry session - 2

14:30-16:00h T1.4-T3.1 (1x6 papers)

16:00-16:15h Refreshment

16:15-18:15h T1.5 // T6.1-T3.2 (2x8 papers)

19:30-22:30h GALA DINNER

08:00-13:00h Registration desk

10:00-11:00h T1.6 // T3.3  (2x4 papers)
11:00-11:30h Invited paper - Prof. Jozef Varga
   Wolong – Sever, Kecskemet, Hungary
   “New indirect measurement procedure for determining regulation
   characteristics of the synchronous machine”

11:30-11:40h Refreshment

11:40-12:20h Key Note 5 - Prof. Dr. Johann W. Kolar
   PES Lab., Swiss Federal Institute of Technology, ETH, Zurich, Switzerland
   “A new generation of three-phase PWM rectifier concepts”

12:20-13:00h Key Note 6 - Dr. Ing. Dusan Graovac
   Infineon Technologies, Neubiberg, Germany
   “Mega trends of automotive electronics and their Influence on
   semiconductors, power electronics and drives”

13:00-13:30h CLOSING Session